

2616

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

HAYASHI et al.

Application Number: 09/976,052

Filed: October 15, 2001

For: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR
INTEGRATED CIRCUIT

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

RECEIVED
NOV 29 2001
Technology Center 2600

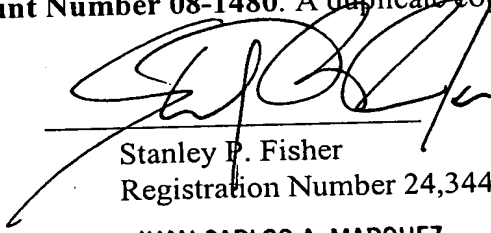
LETTER

Sir:

The below-identified communications are submitted in the above-captioned application or proceeding:

<input checked="" type="checkbox"/> (X)	Priority Documents (P)	<input type="checkbox"/> ()	Verified English Translation
<input checked="" type="checkbox"/> (X)	Notice of Priority	<input type="checkbox"/> ()	Information Disclosure Statement
<input type="checkbox"/> ()	Response to Missing Parts with executed declaration	<input type="checkbox"/> ()	Notice of Related Applications
		<input type="checkbox"/> ()	Check for \$.00

The Commissioner is hereby authorized to charge payment of any fees associated with this communication, including fees under 37 C.F.R. § 1.16 and 1.17 or credit any overpayment to **Deposit Account Number 08-1480**. A duplicate copy of this sheet is attached.


Stanley P. Fisher
Registration Number 24,344

JUAN CARLOS A. MARQUEZ
Registration No. 34,072

REED SMITH HAZEL & THOMAS LLP
3110 Fairview Park Drive
Suite 1400
Falls Church, Virginia 22042
(703) 641-4200

November 26, 2001

RECEIVED
DEC-4 2001
TC 2800 MAIL ROOM